

Notice of References Cited	Application/Control No. 09/540,729	Applicant(s)/Patent Under Reexamination GURA ET AL.	
	Examiner Dmitry Levitan	Art Unit 2662	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Cell-level path allocation in a three-stage ATM switch Collier, M.; Curran, T.; Communications, 1994. ICC 94, SUPERCOMM/ICC '94, Conference Record, Serving Humanity Through Communications. IEEE International Conference on , 1-5 May 1994 Pages:1179 - 1183
	V	Path allocation in a three-stage broadband switch with intermediate channel grouping Collier, M.; Curran, T.; INFOCOM '93. Proceedings. Twelfth Annual Joint Conference of the IEEE Computer and Communications Societies. IEEE, 28 March- April 1993, p. 927-934
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.